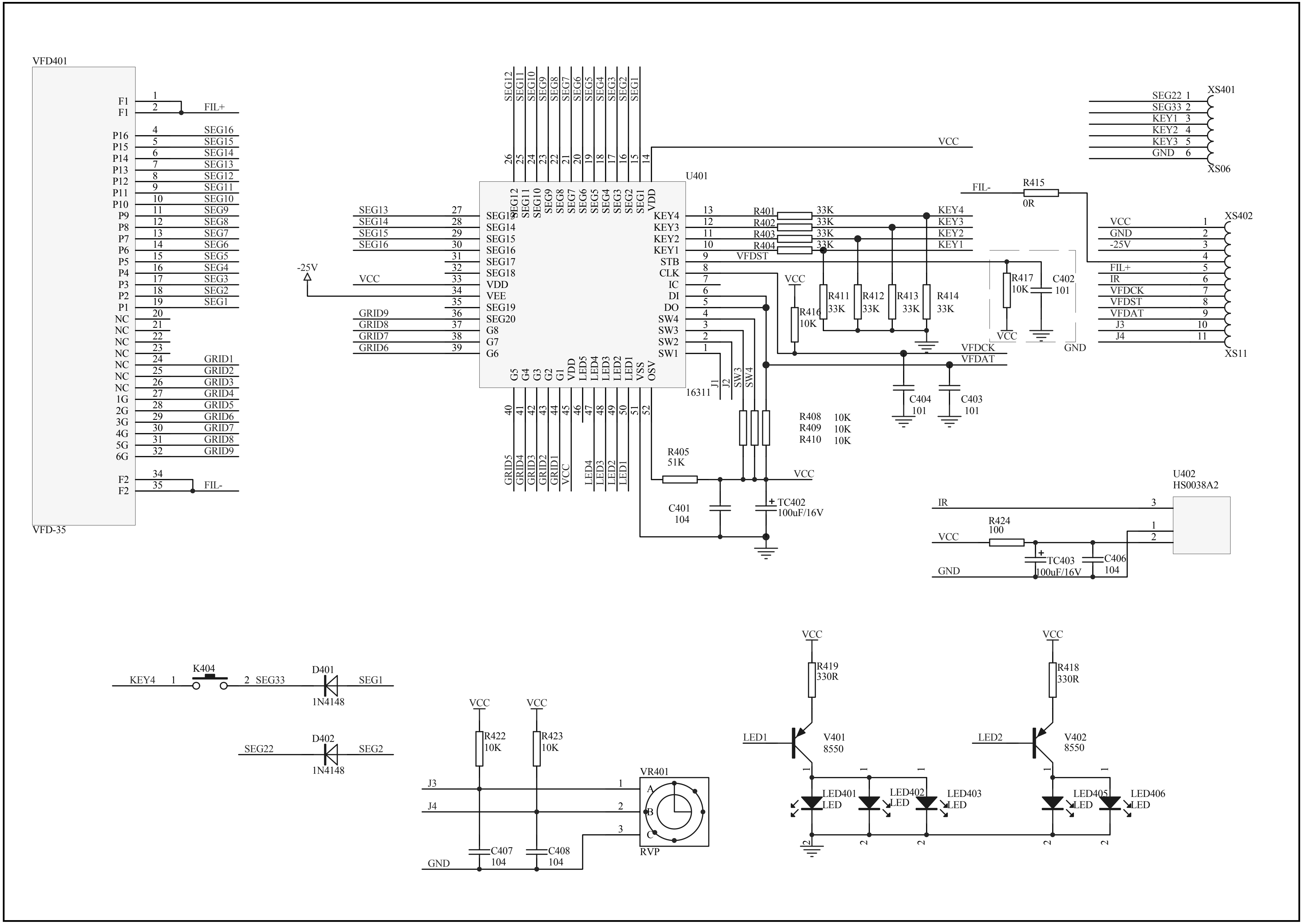
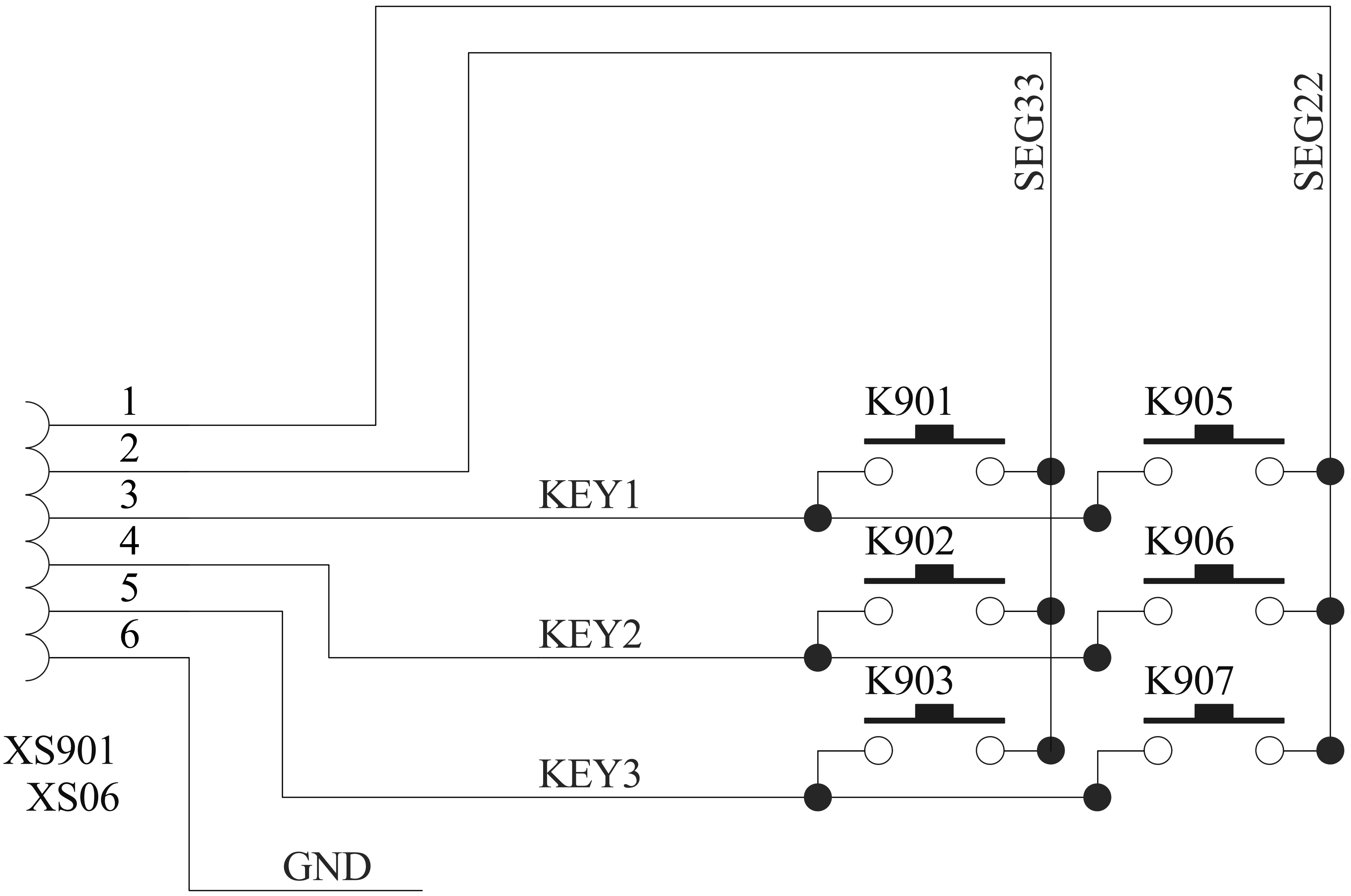
9.SCHEMATIC & P.C.B WIRING DIAGRAM

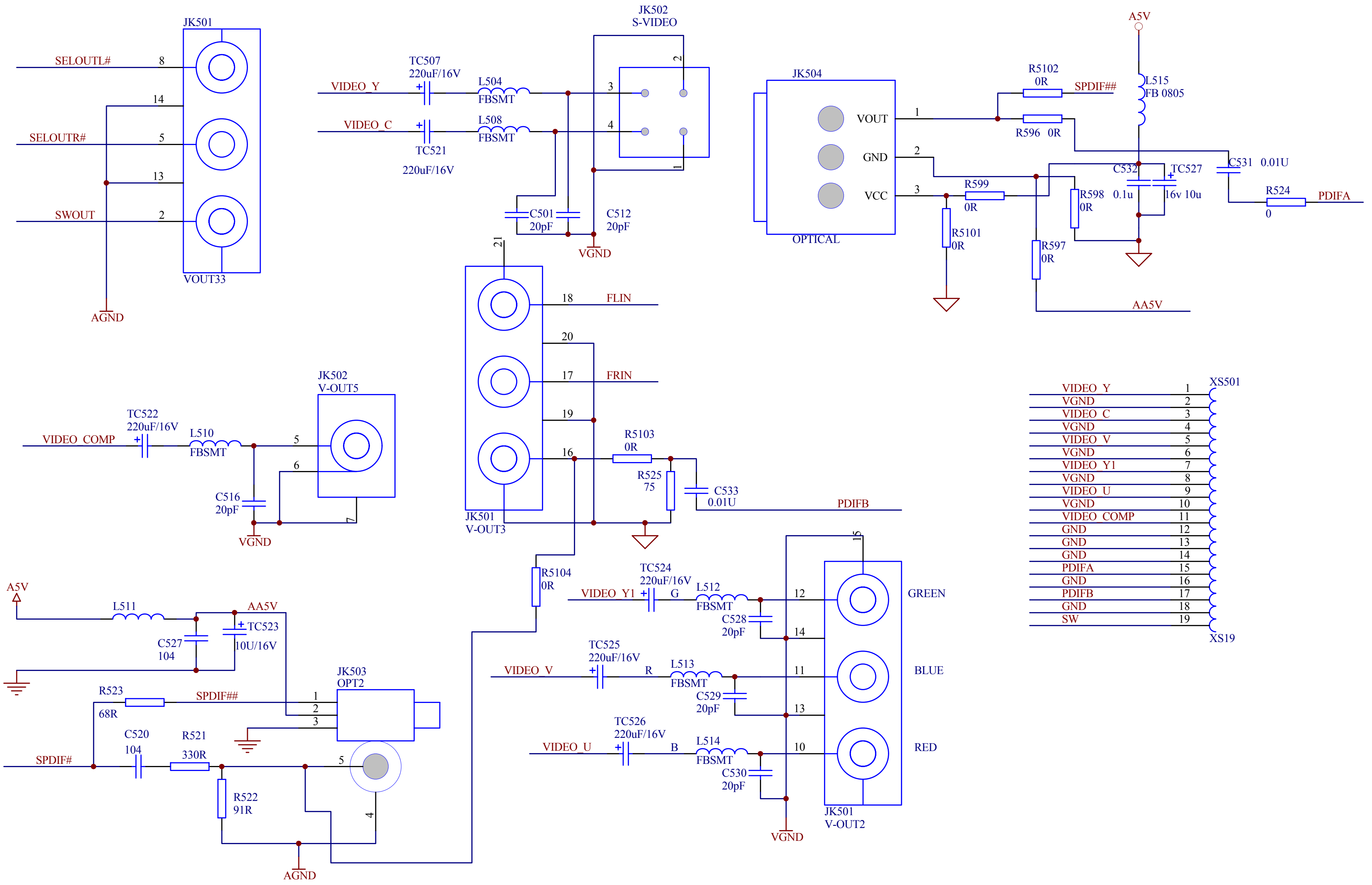
FRONT SCHEMATIC DIAGRAM



SUBSIDIARY FRONT SCHEMATIC DIAGRAM



POWER SOURCE/AMPLIFIER BOARD SCHEMATIC DIAGRAM



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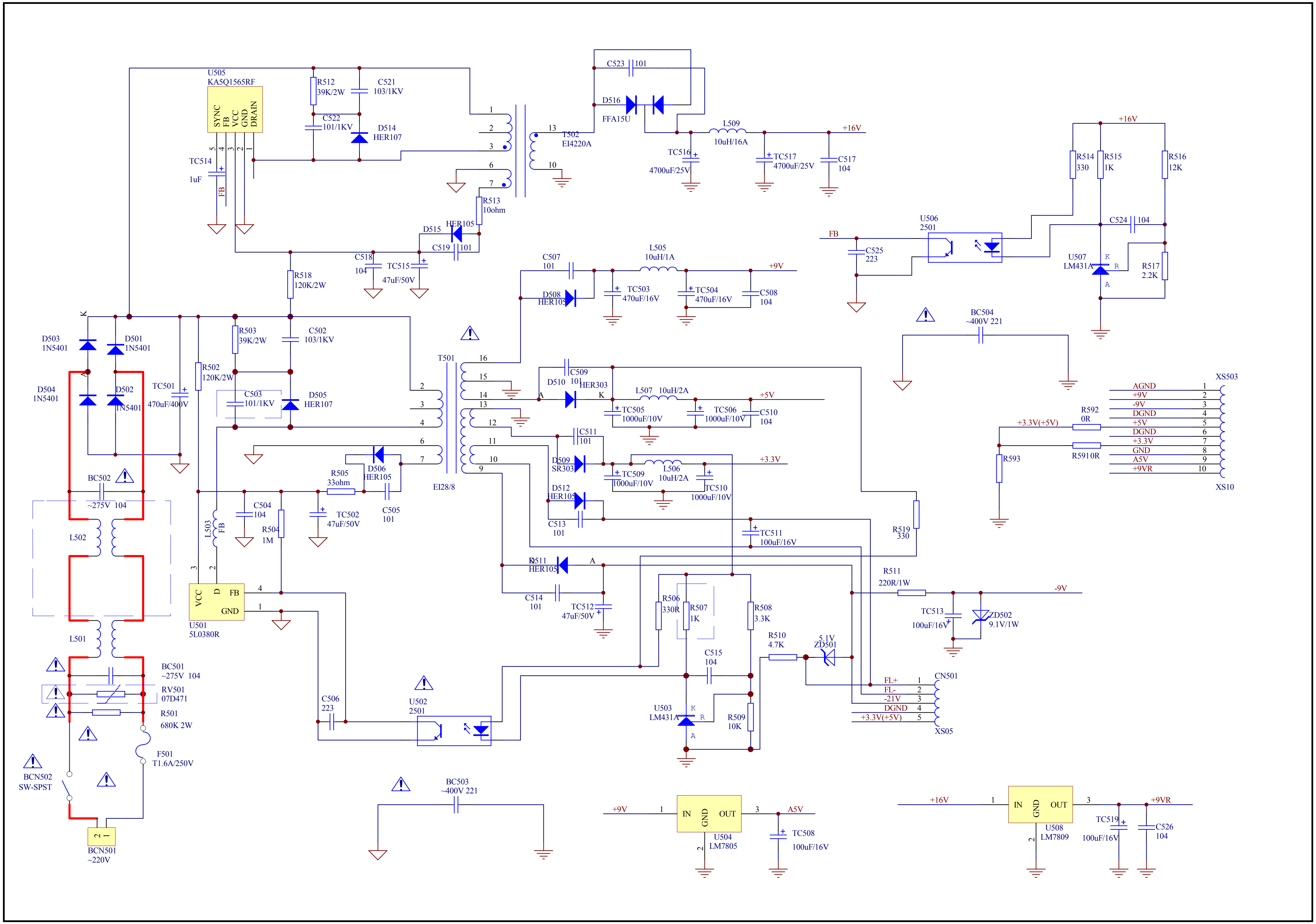

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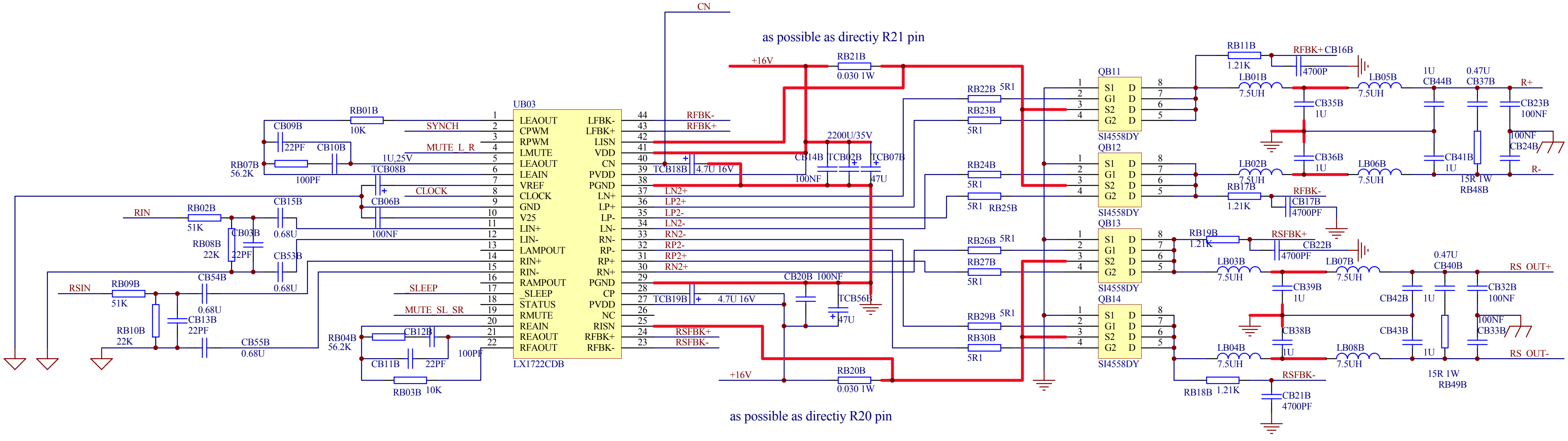

as possible as directiy R21 pin

as possible as directiy R20 pin

NOTE:

1. AGND and GND plate is expected,they are devided by the center of LX1721.
2. Red havey line means needing heavy duty trace or copper.
3. Heat sink need to be solder on the AGND plate.
4. LPWM+,LPWM-,RPWM+,RPWM- must be kept as short as possible
5. Chassis gnd circles the whole board on both top and bottom layer.

LN+,LP+,LP-,LN-RN-,RP-RP+,RN+, keep as short as possible

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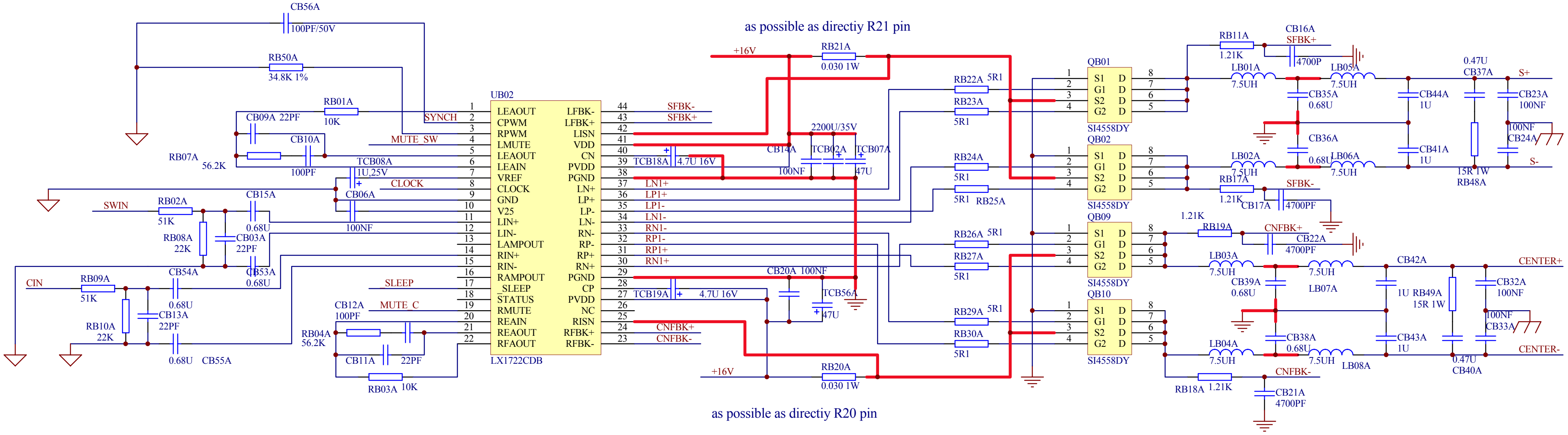

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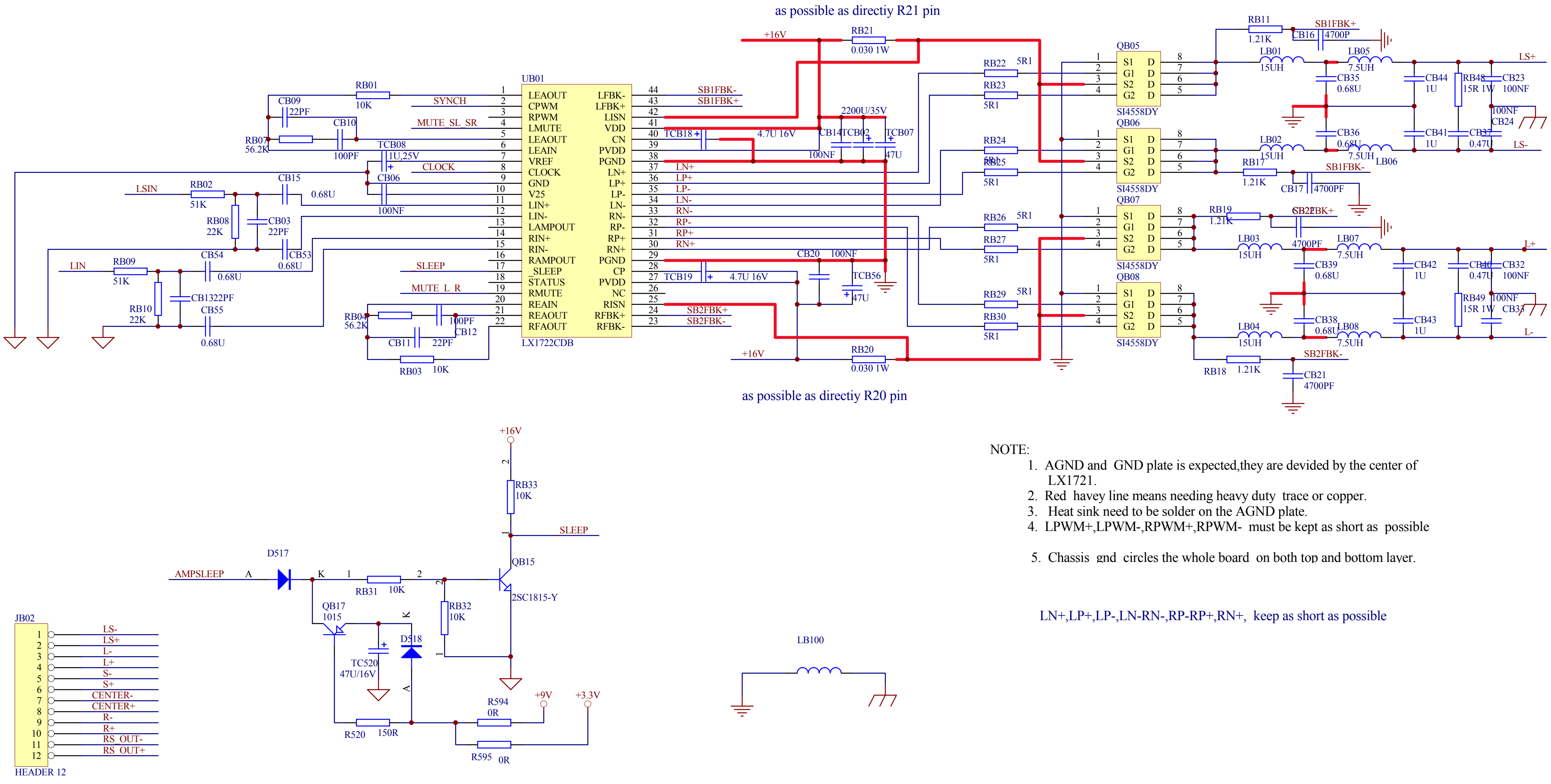

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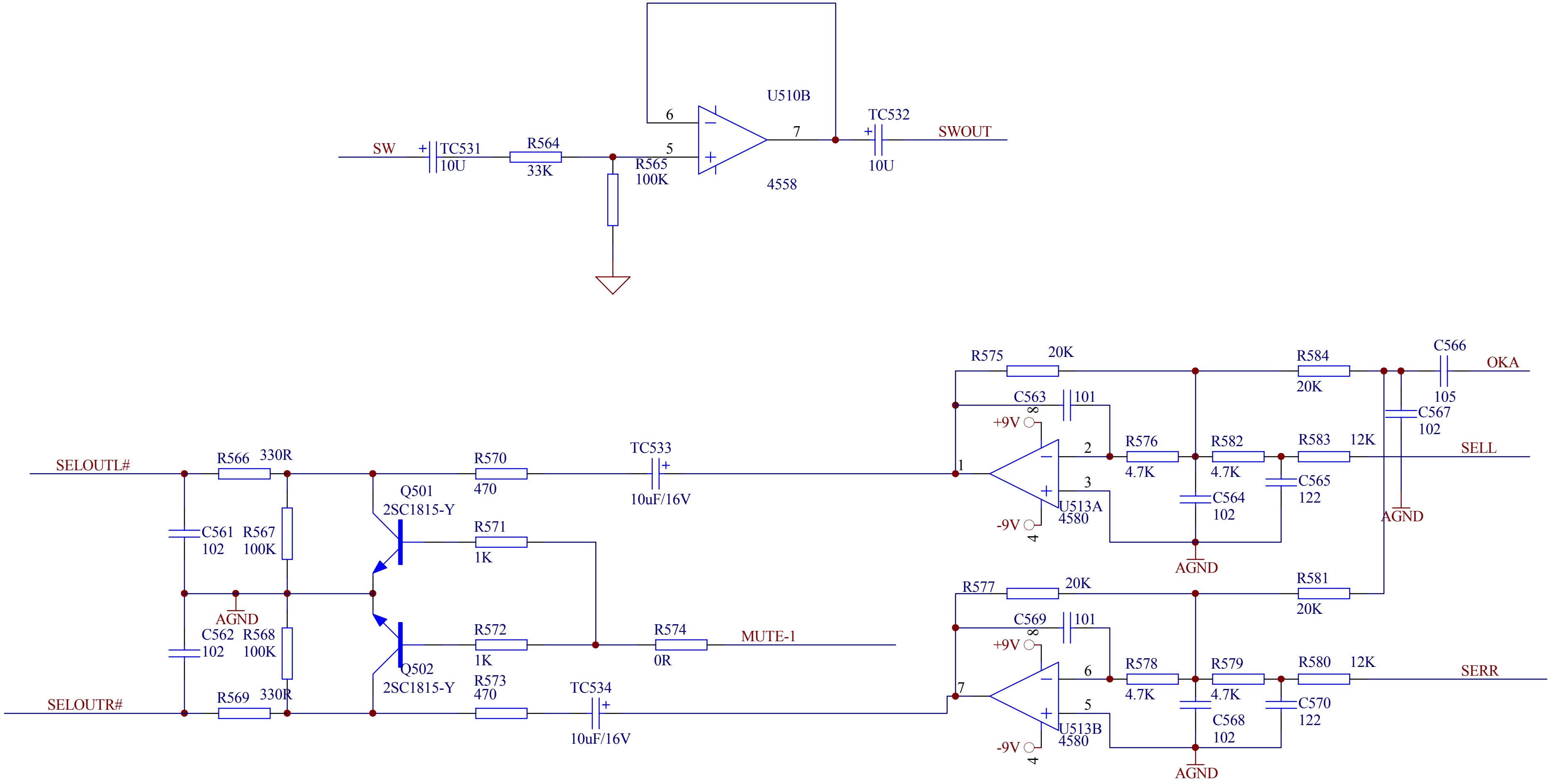

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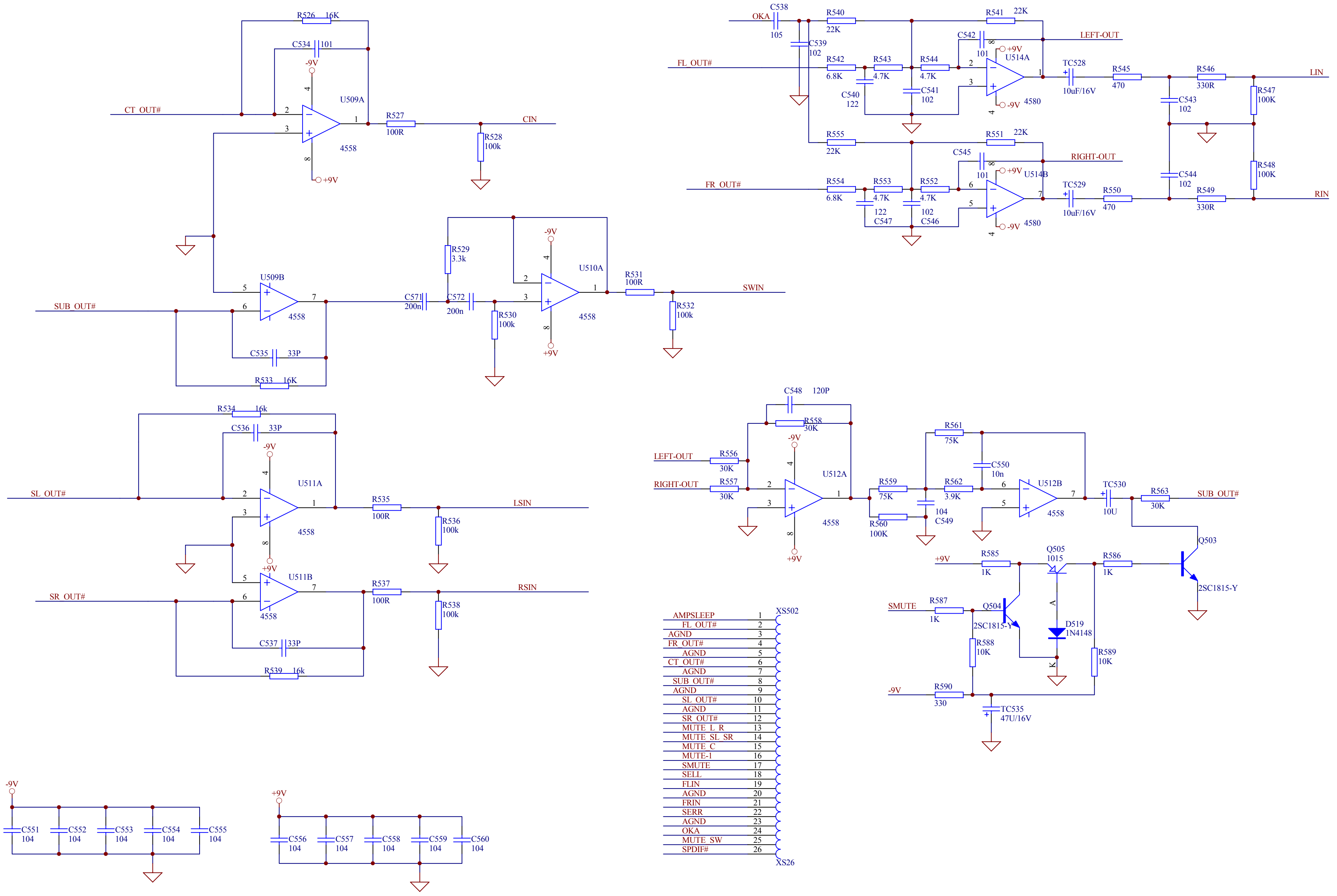
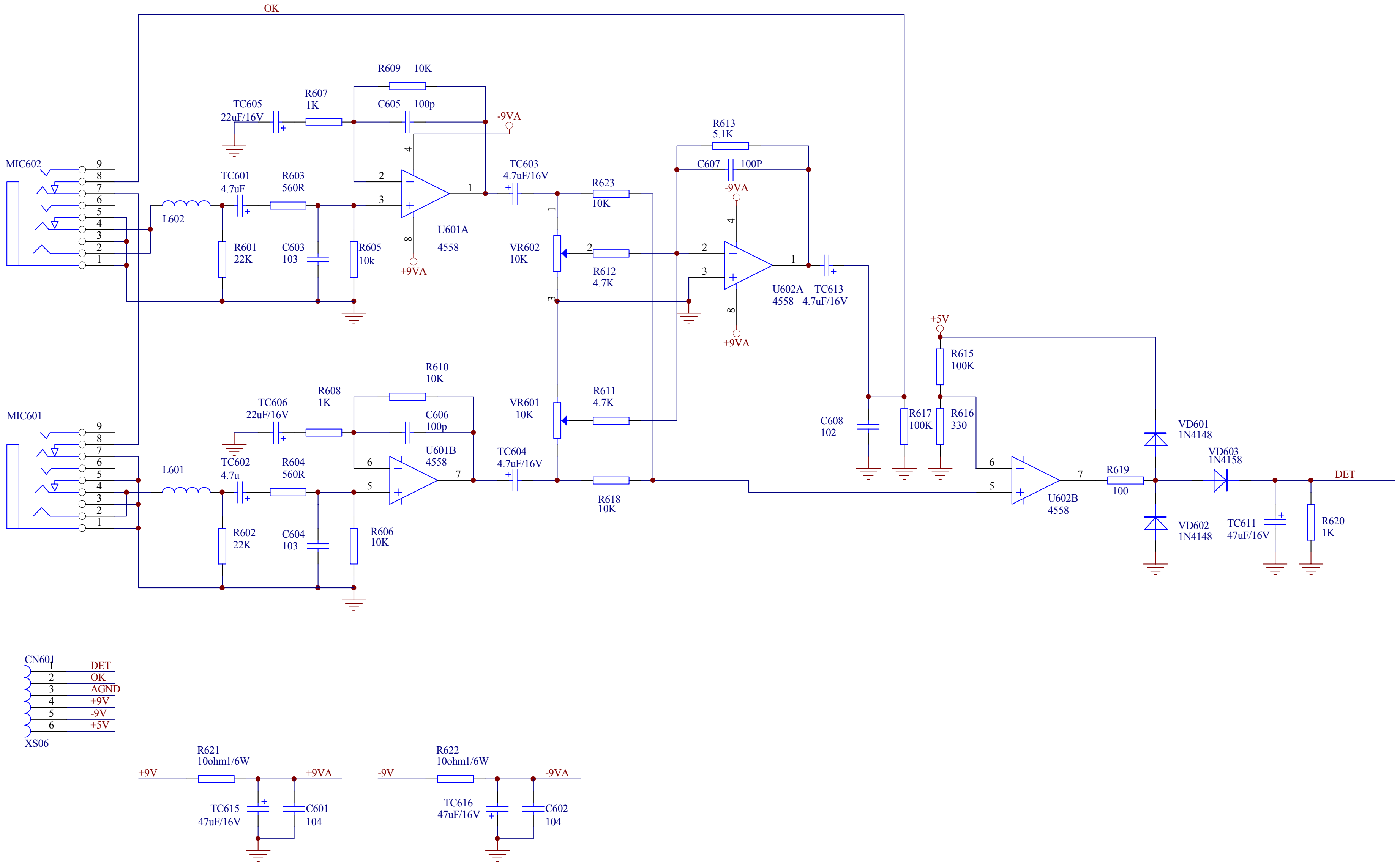
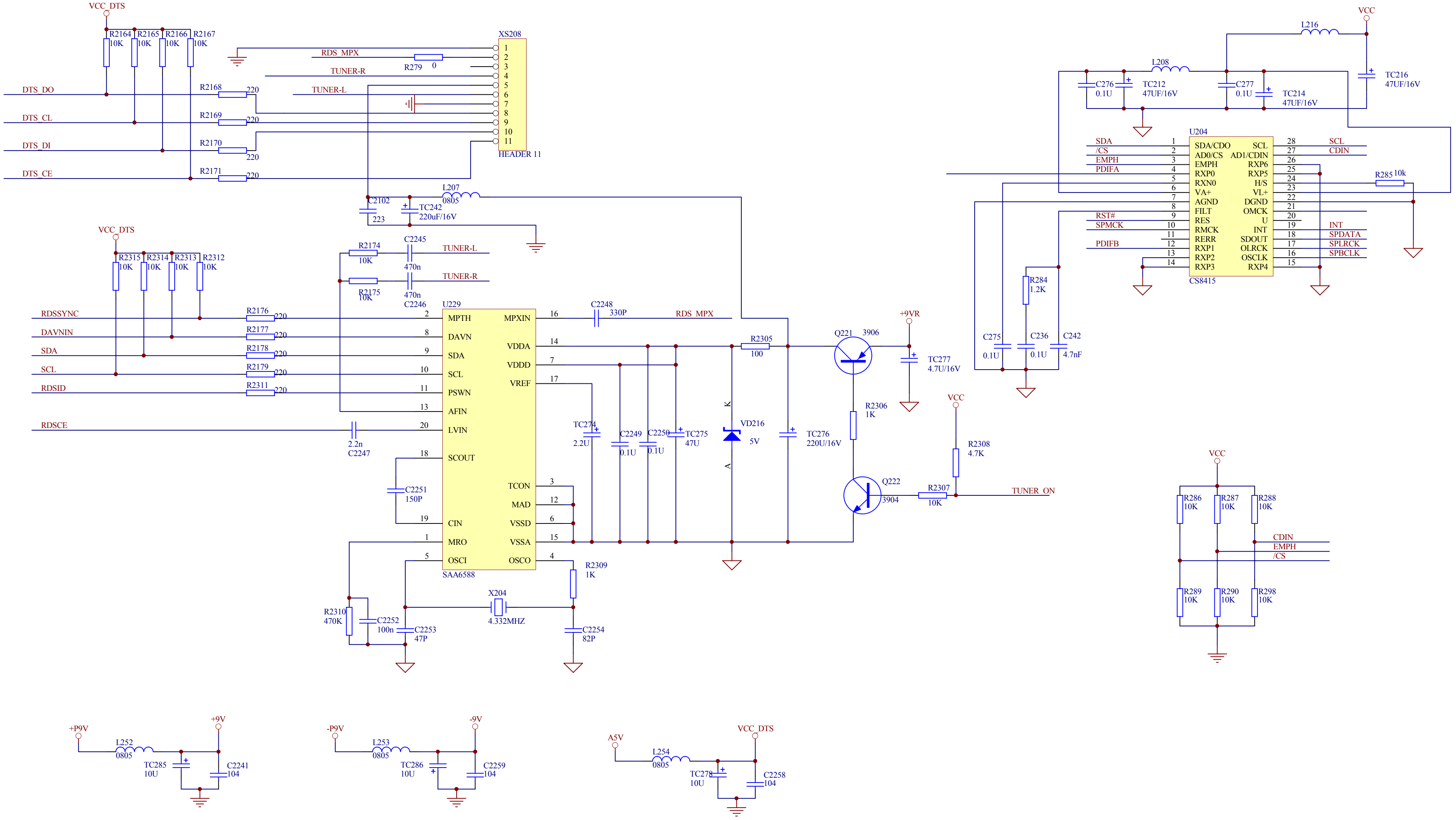


OK SCHEMATIC DIAGRAM



MIAN SCHEMATIC DIAGRAM



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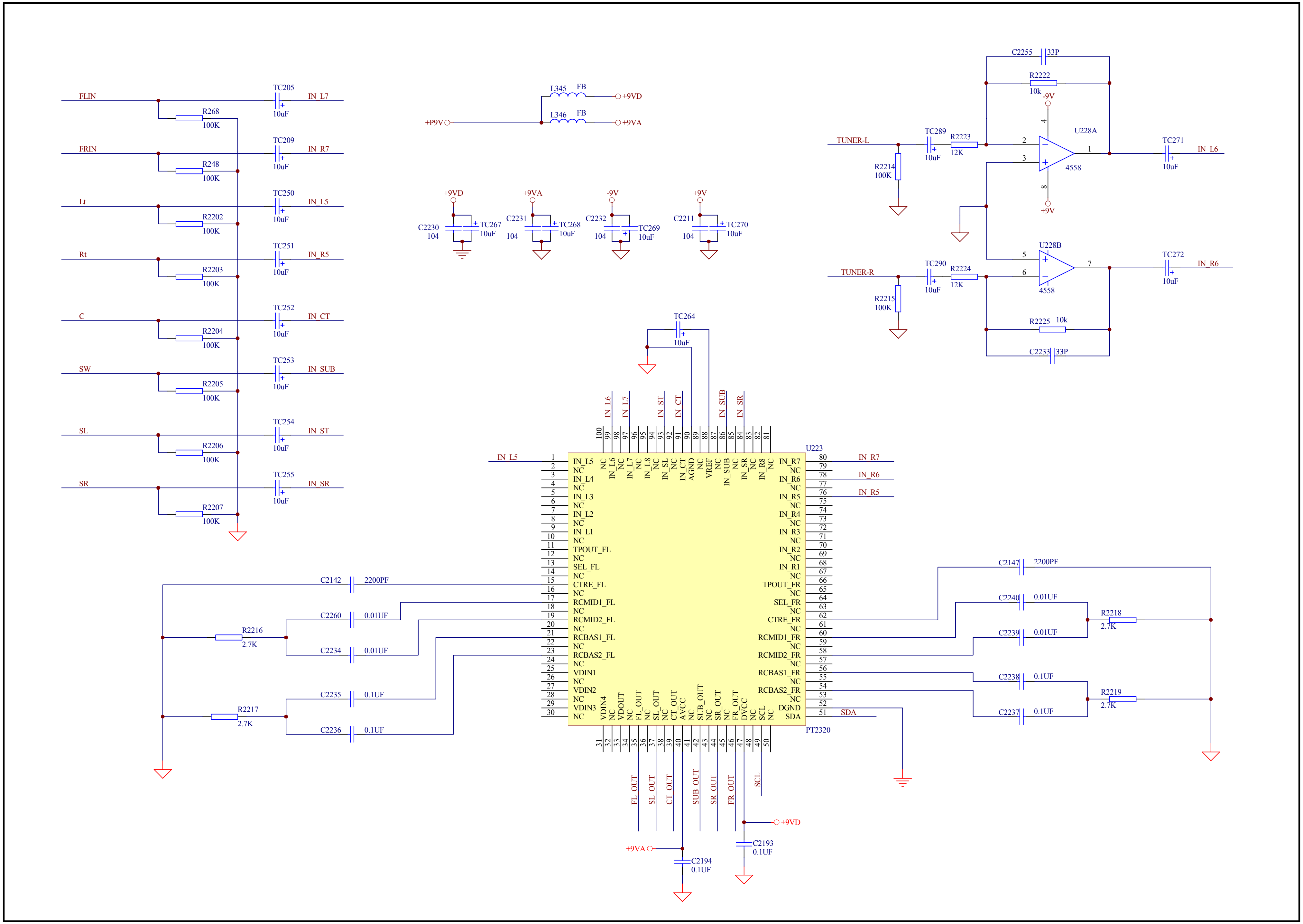

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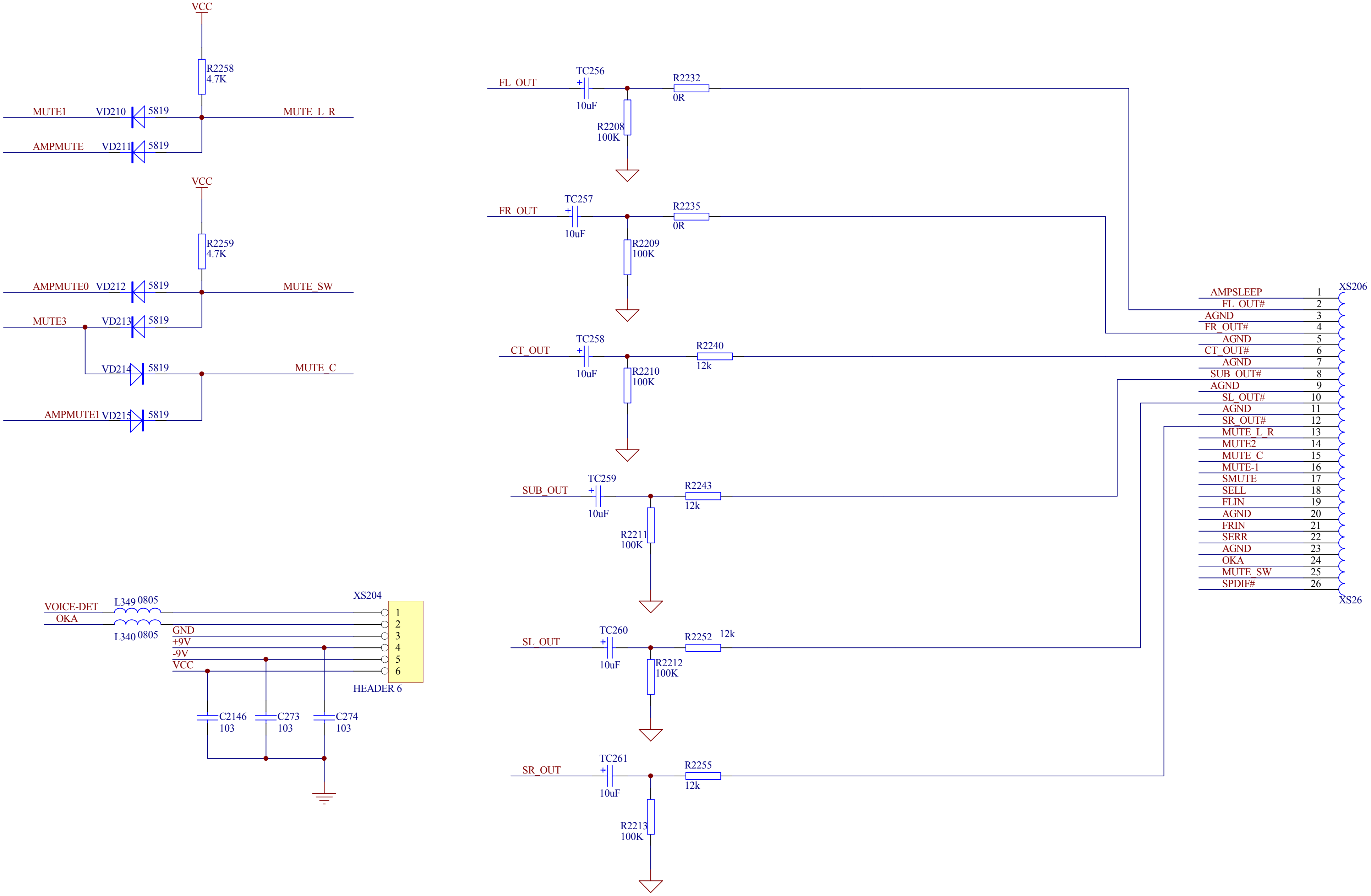

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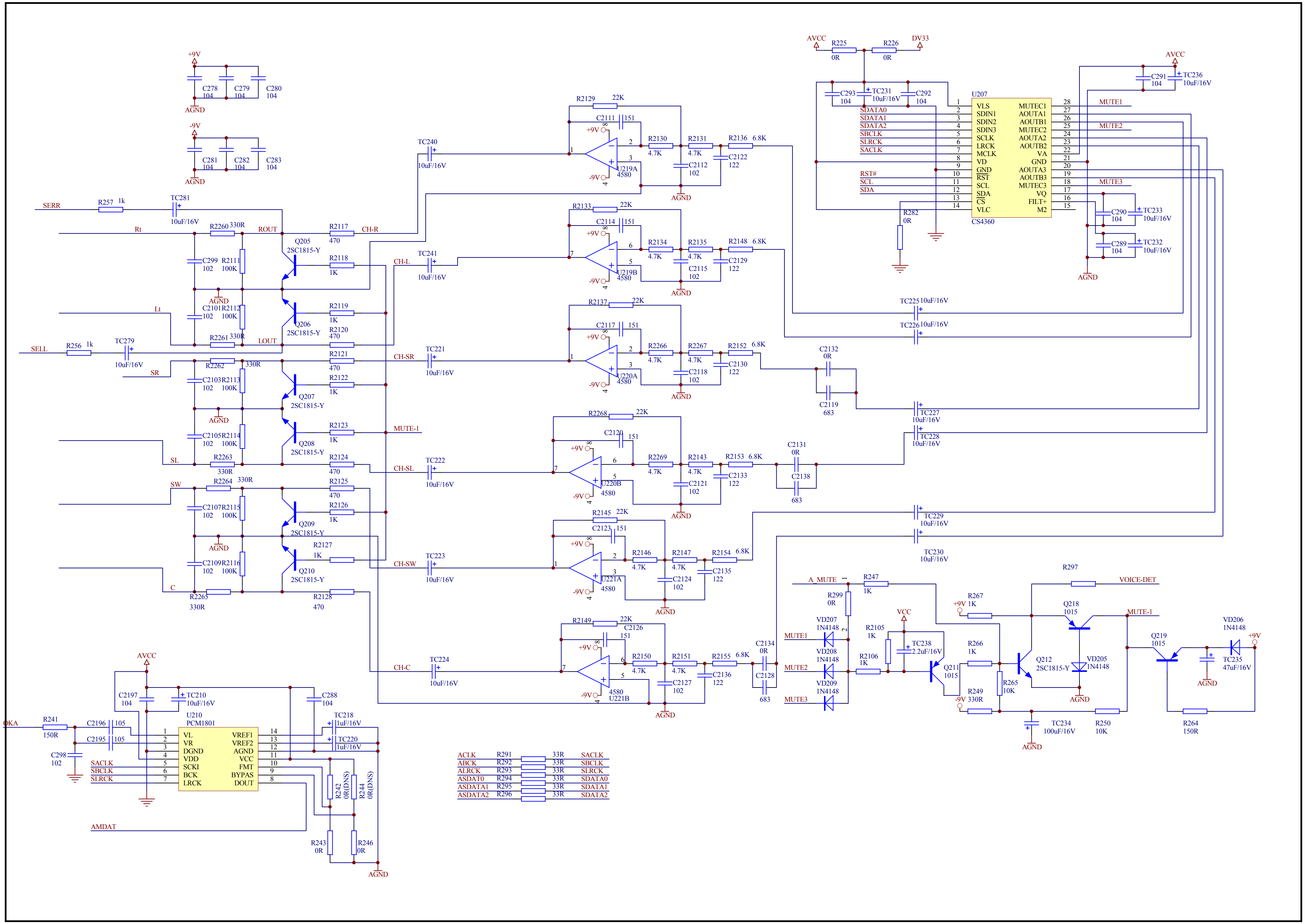

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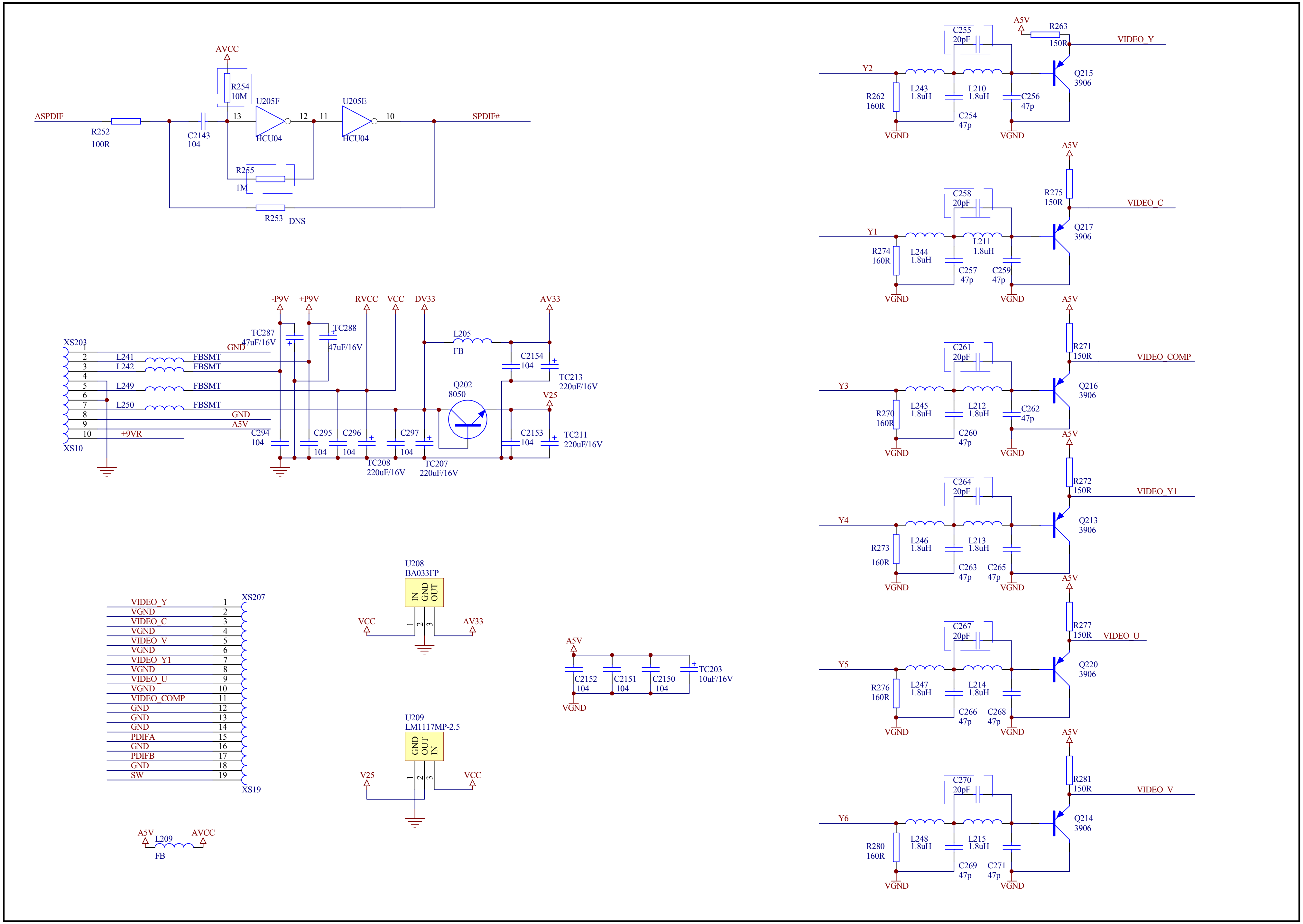

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